

1. An apparatus for generating delta/sigma modulation quantized data representing the ratio of two frequencies F_x and F_s , comprising:

- a counter circuit for counting the number of clock edges of F_s occurring in the time period between clock edges of F_x ; and
- a register circuit for storing the value of the counter circuit at each clock edge of F_s ;

the sequence of values from the register circuit constituting the delta/sigma modulation quantized data.